

1/14

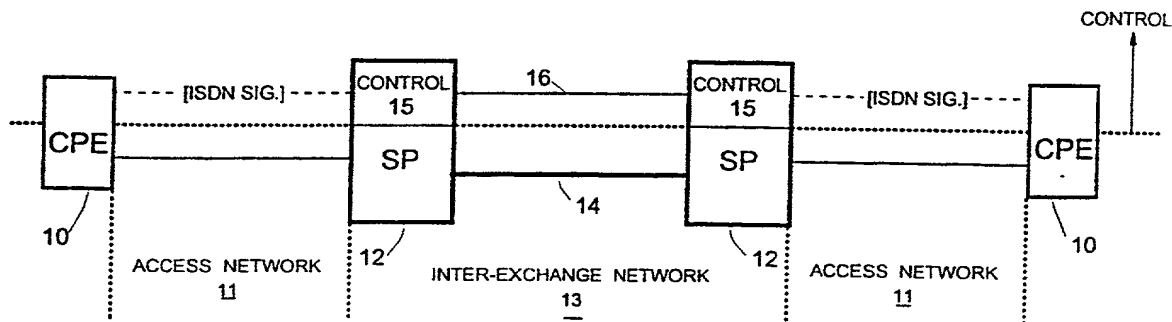


FIG. 1
(PRIOR ART)

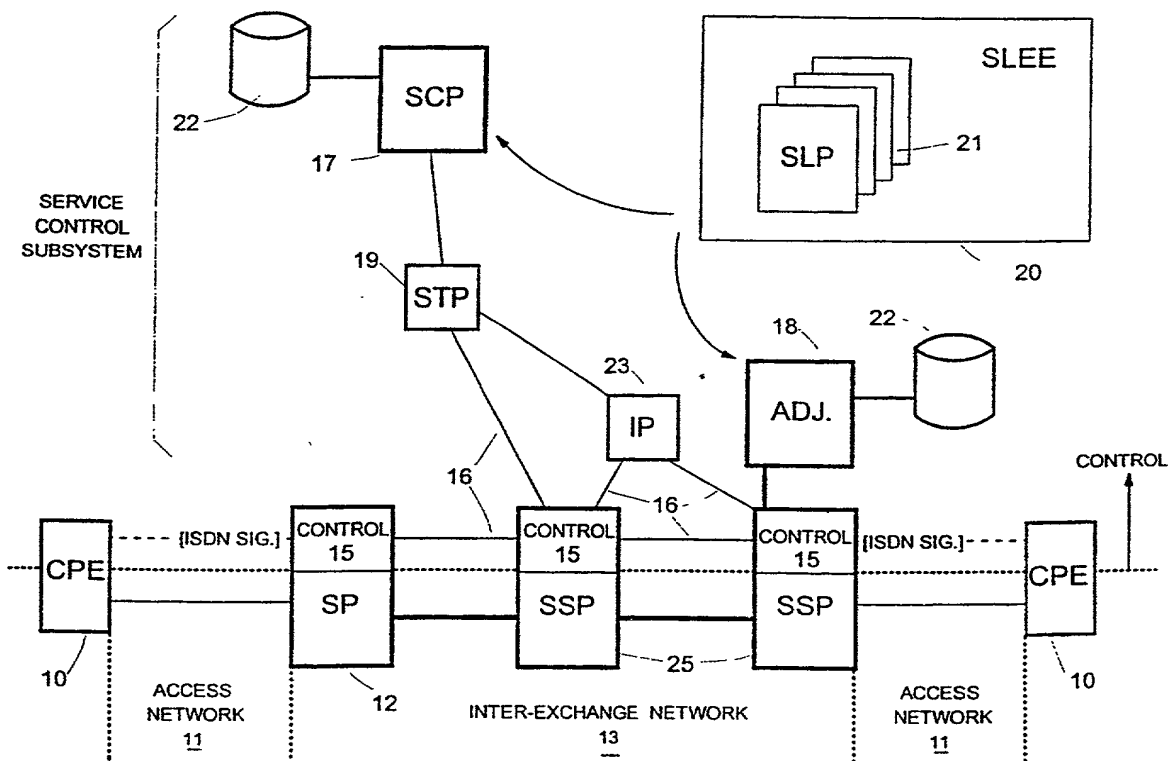


FIG. 2
(PRIOR ART)

2/14

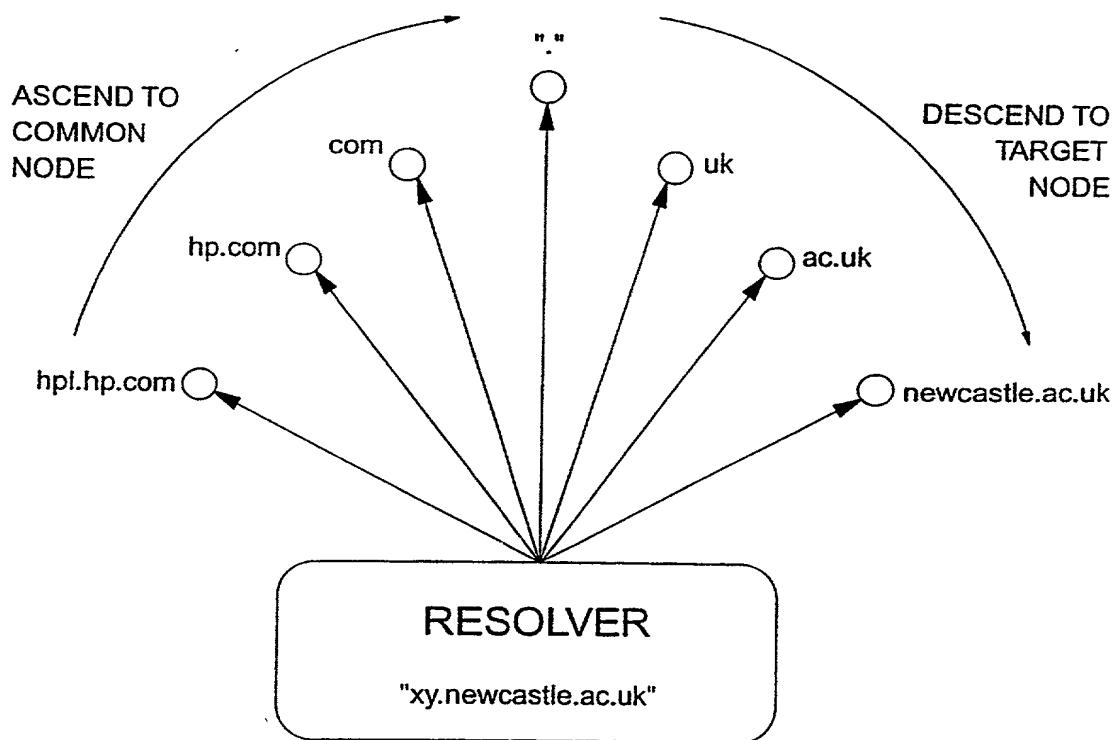
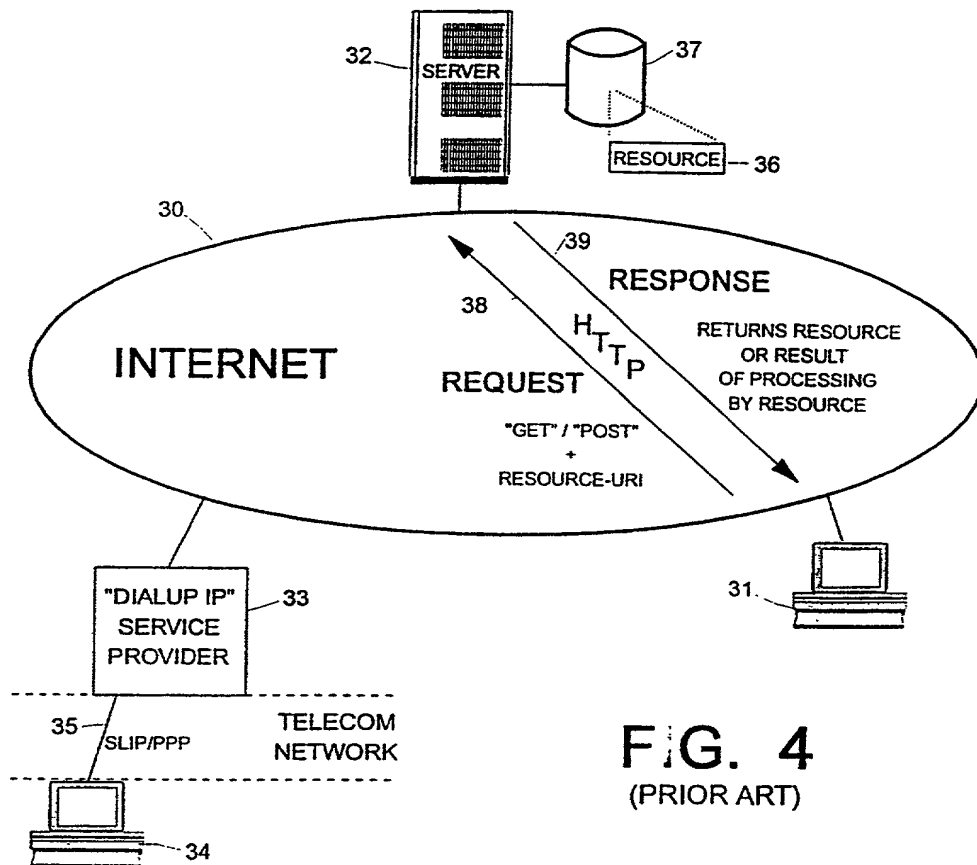


FIG. 3
(PRIOR ART)

3/14

FIG. 4
(PRIOR ART)

[http : //www.hp.com/Products.html](http://www.hp.com/Products.html)

SCHEME

HOST LOCATION

ABSOLUTE PATH

FIG. 5
(PRIOR ART)

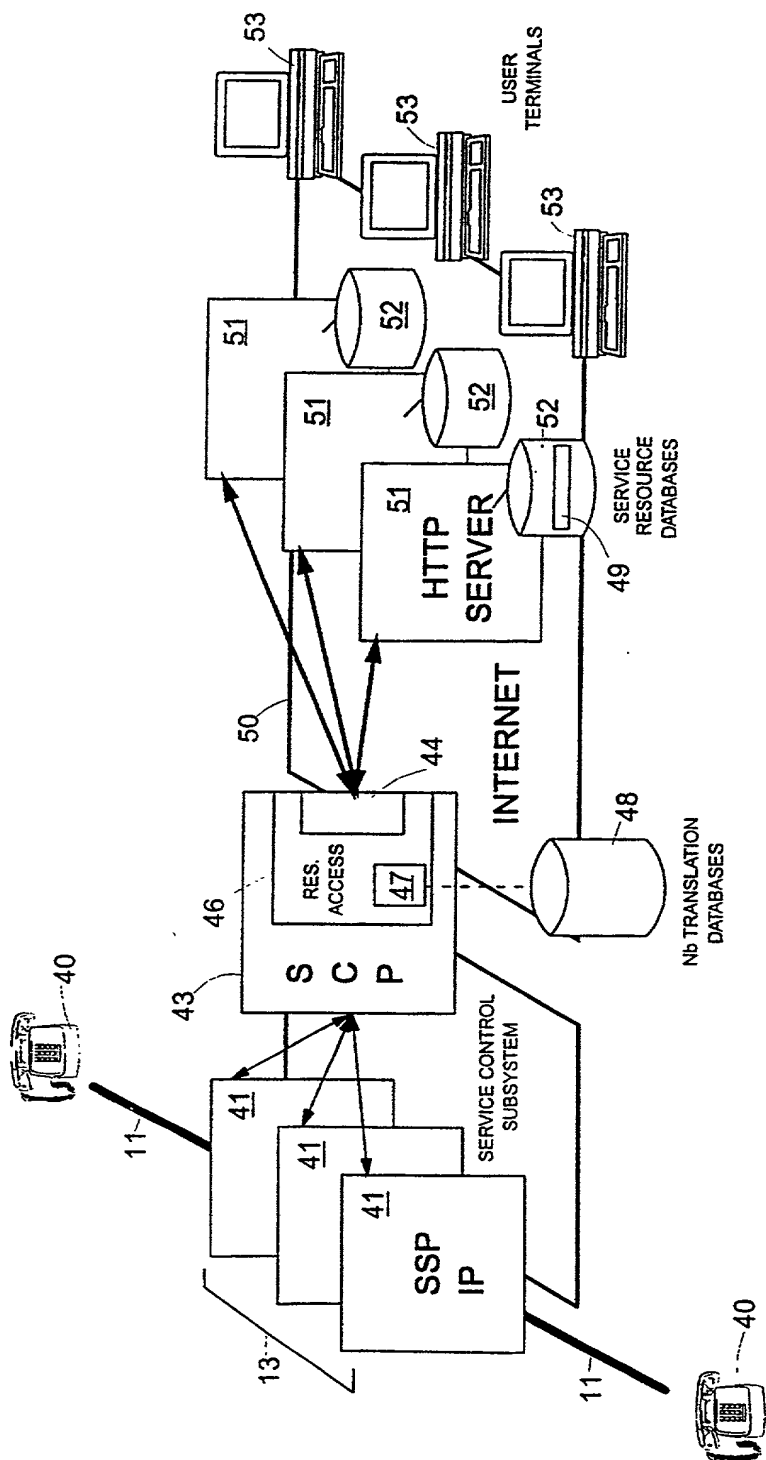


FIG. 6

5/14

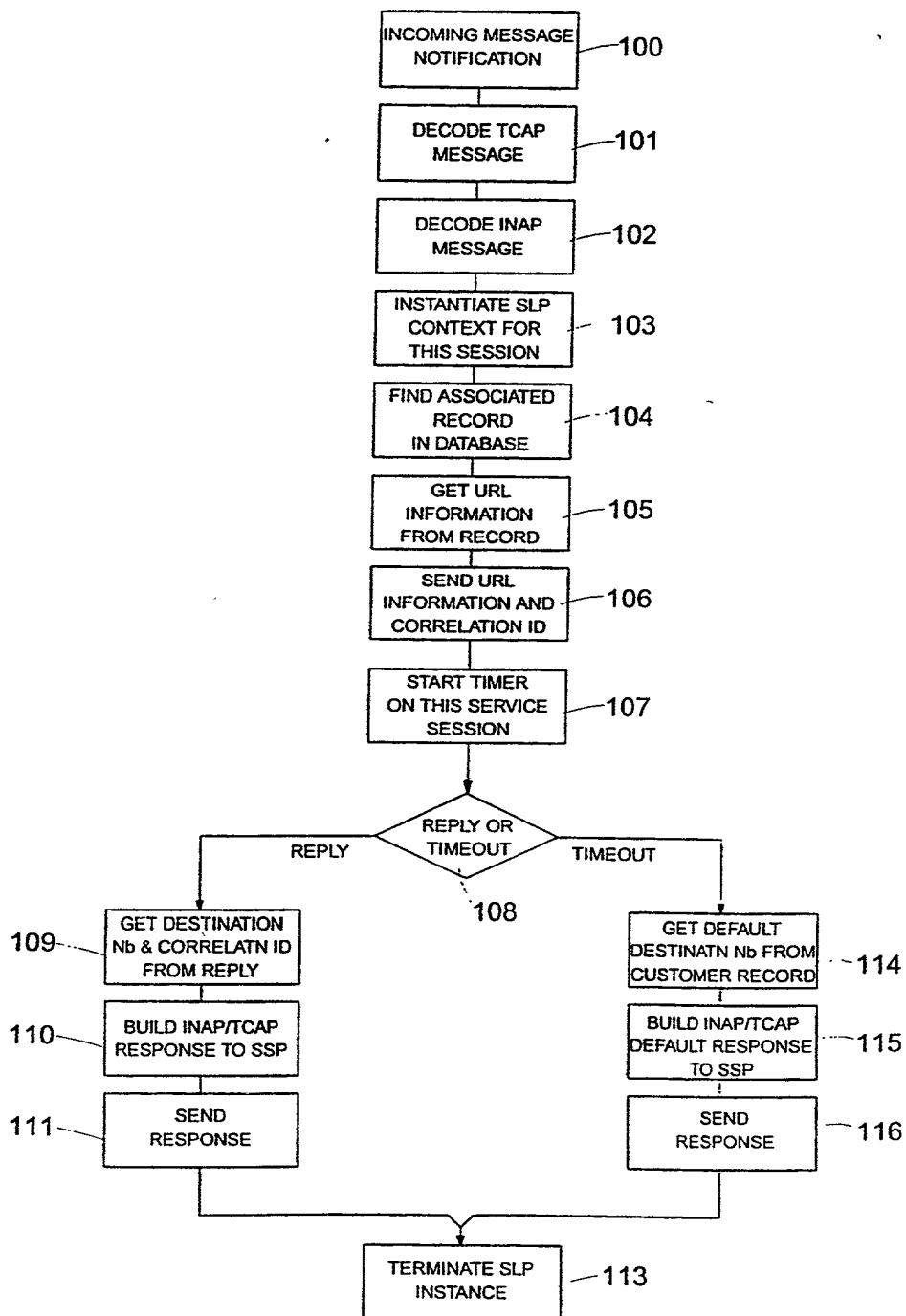


FIG. 7

6/14

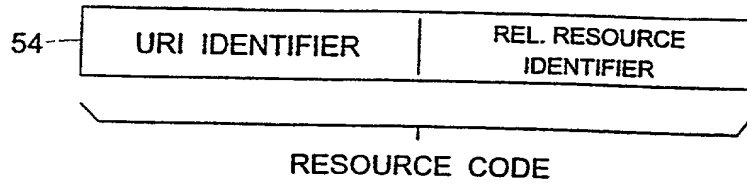


FIG. 8

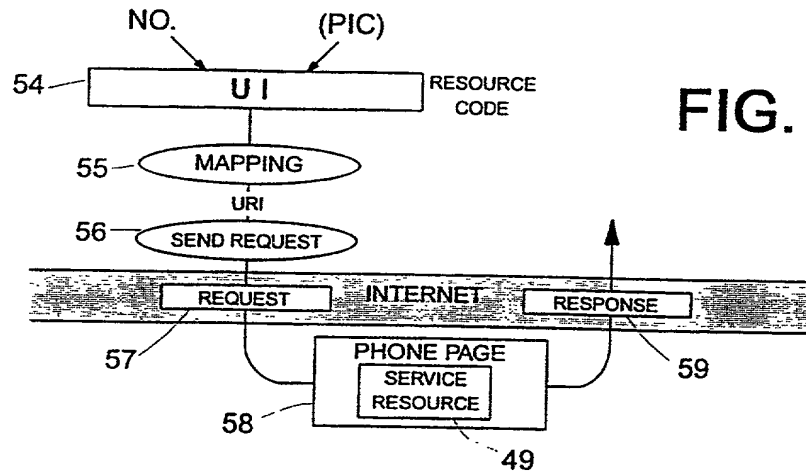


FIG. 9

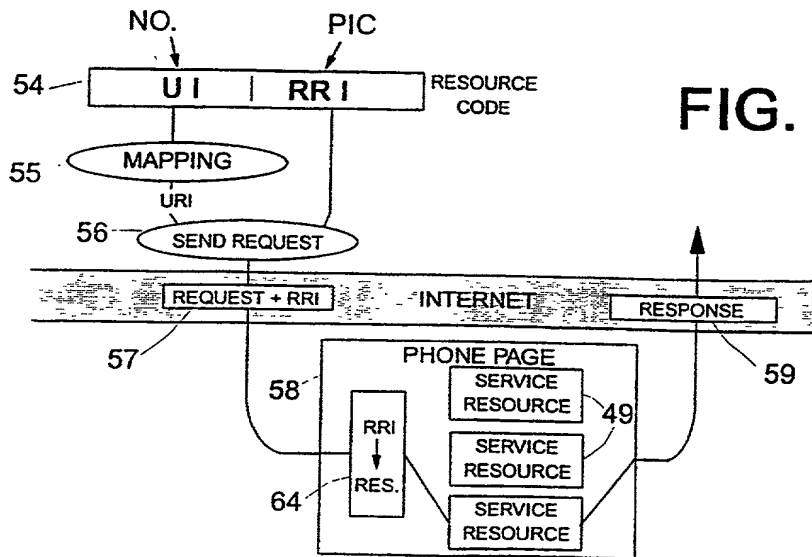


FIG. 10

2025-01-10 10:05:00

7/14

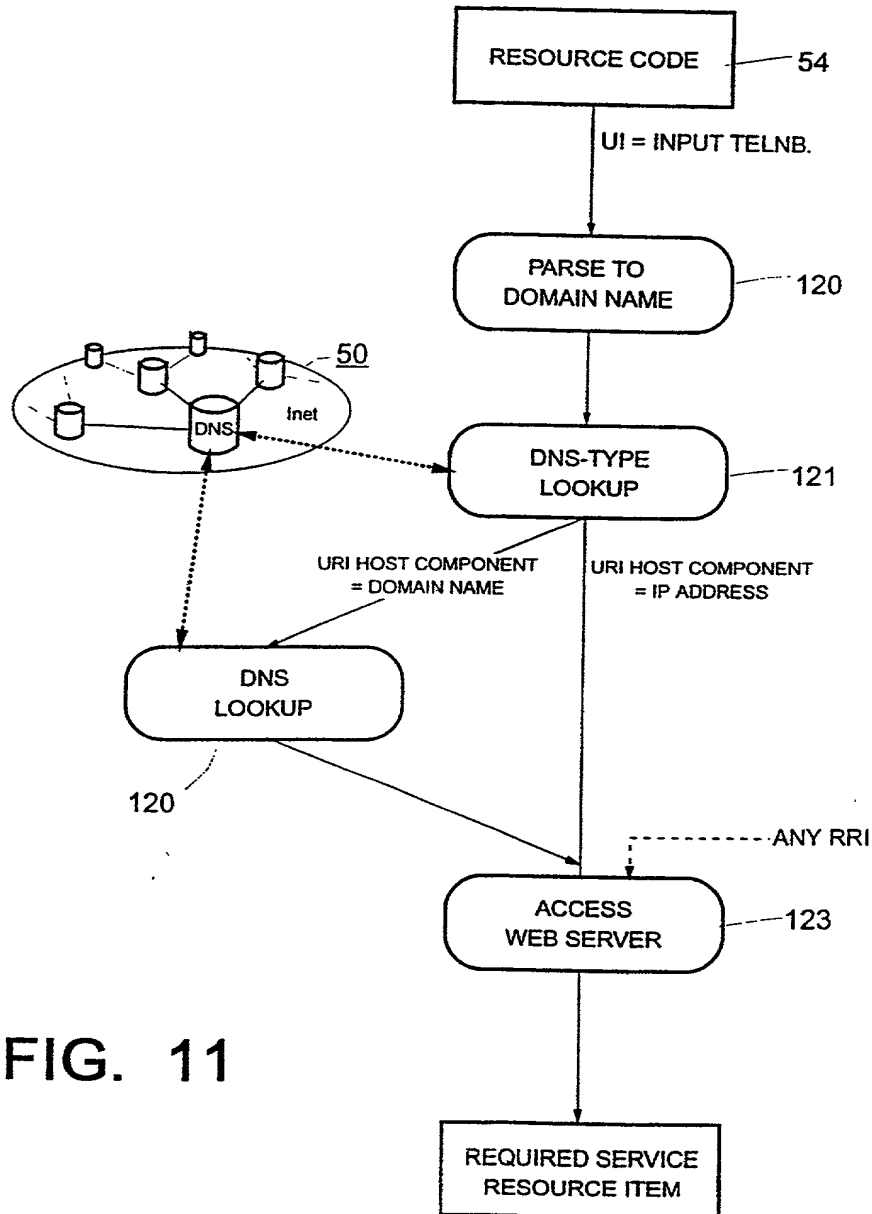


FIG. 11

8/14

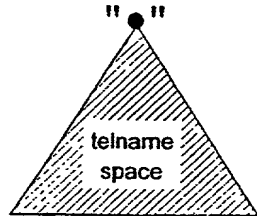


FIG. 12A

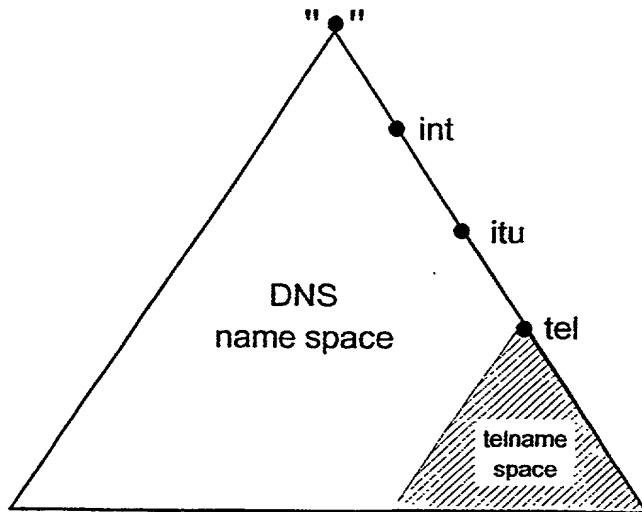


FIG. 12B

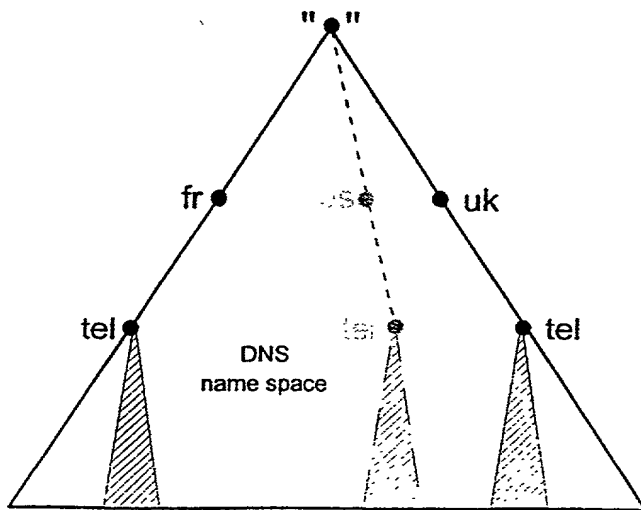


FIG. 12C

208710 50E2500T

9/14

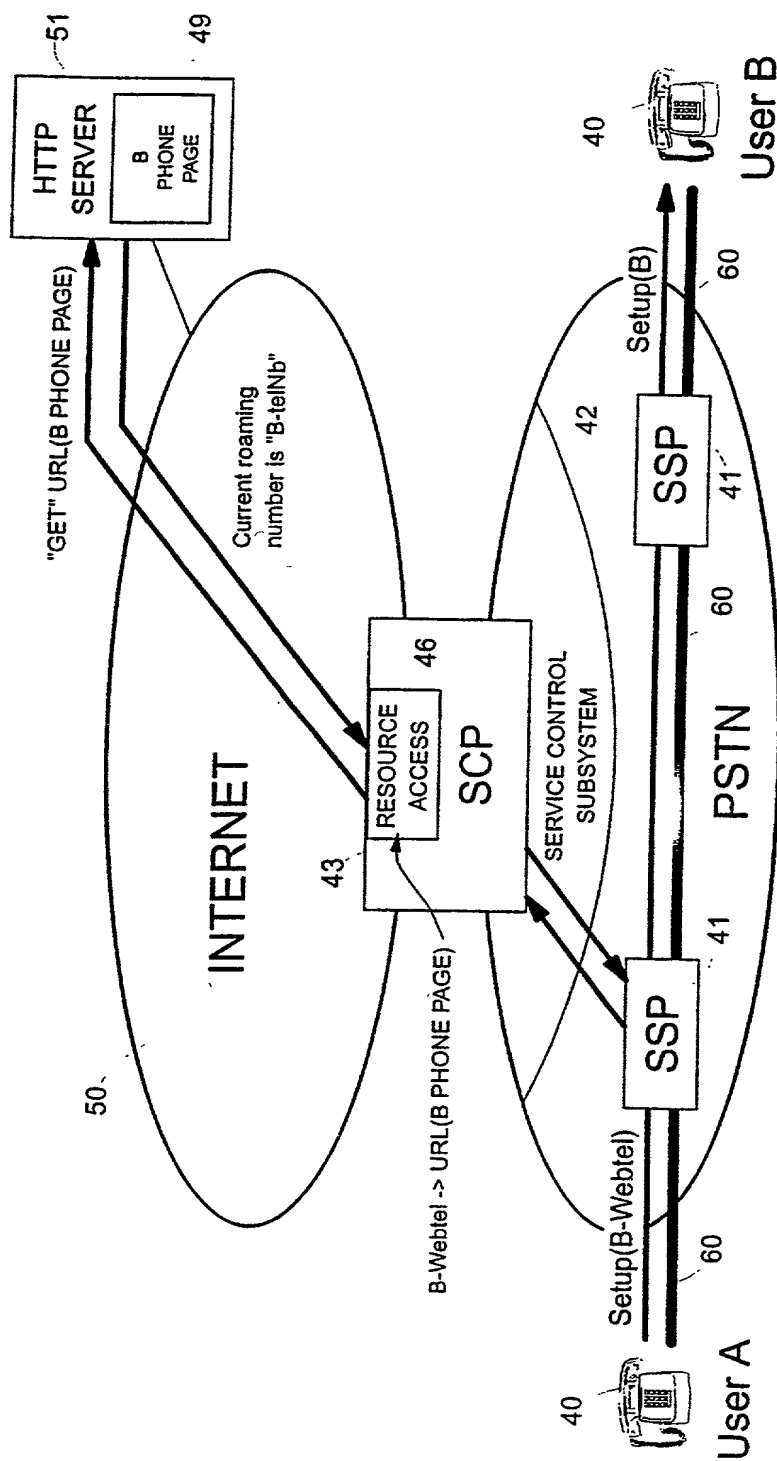


FIG. 13

10/14

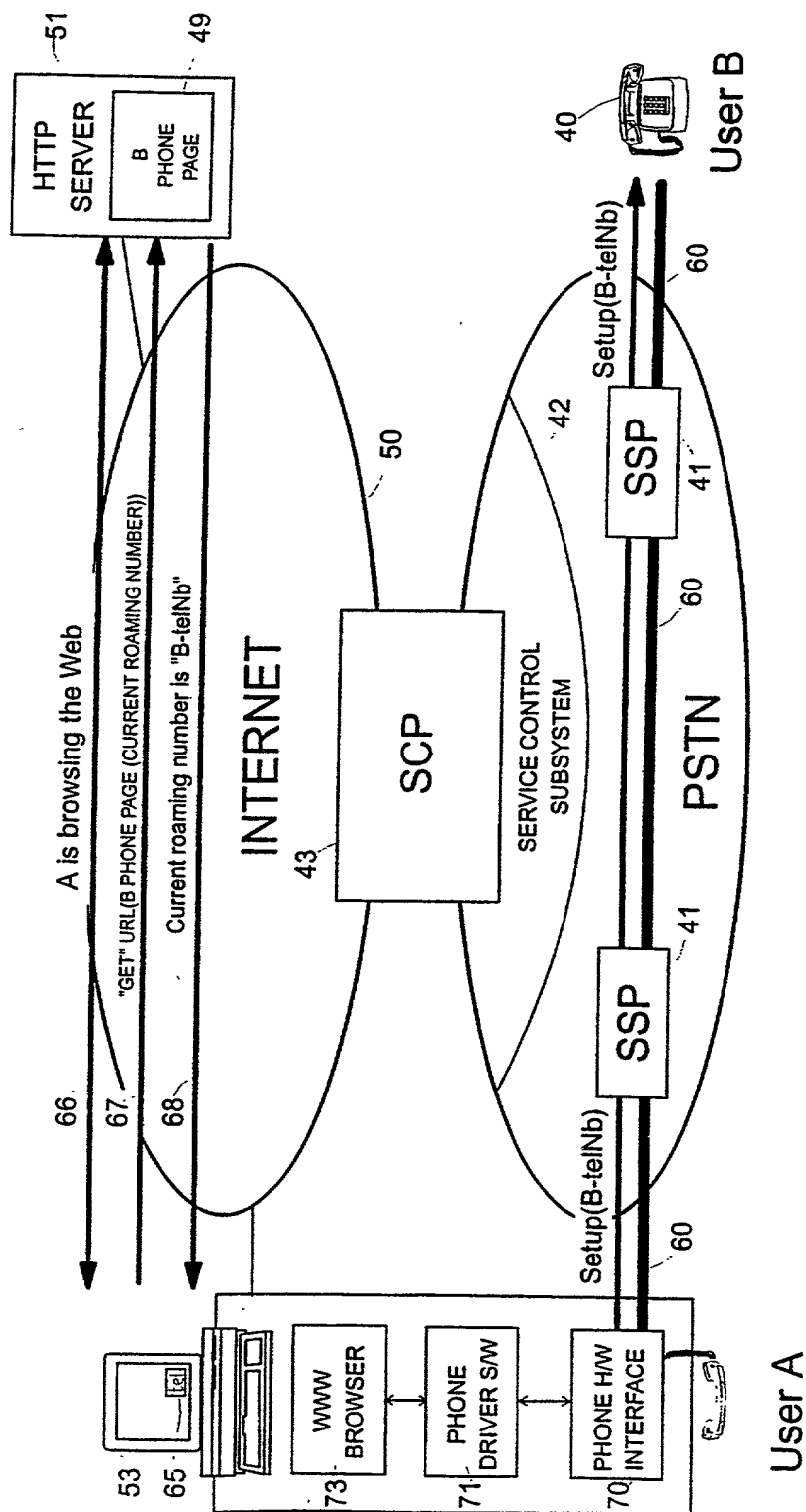


FIG. 14

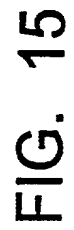


FIG. 15

12/14

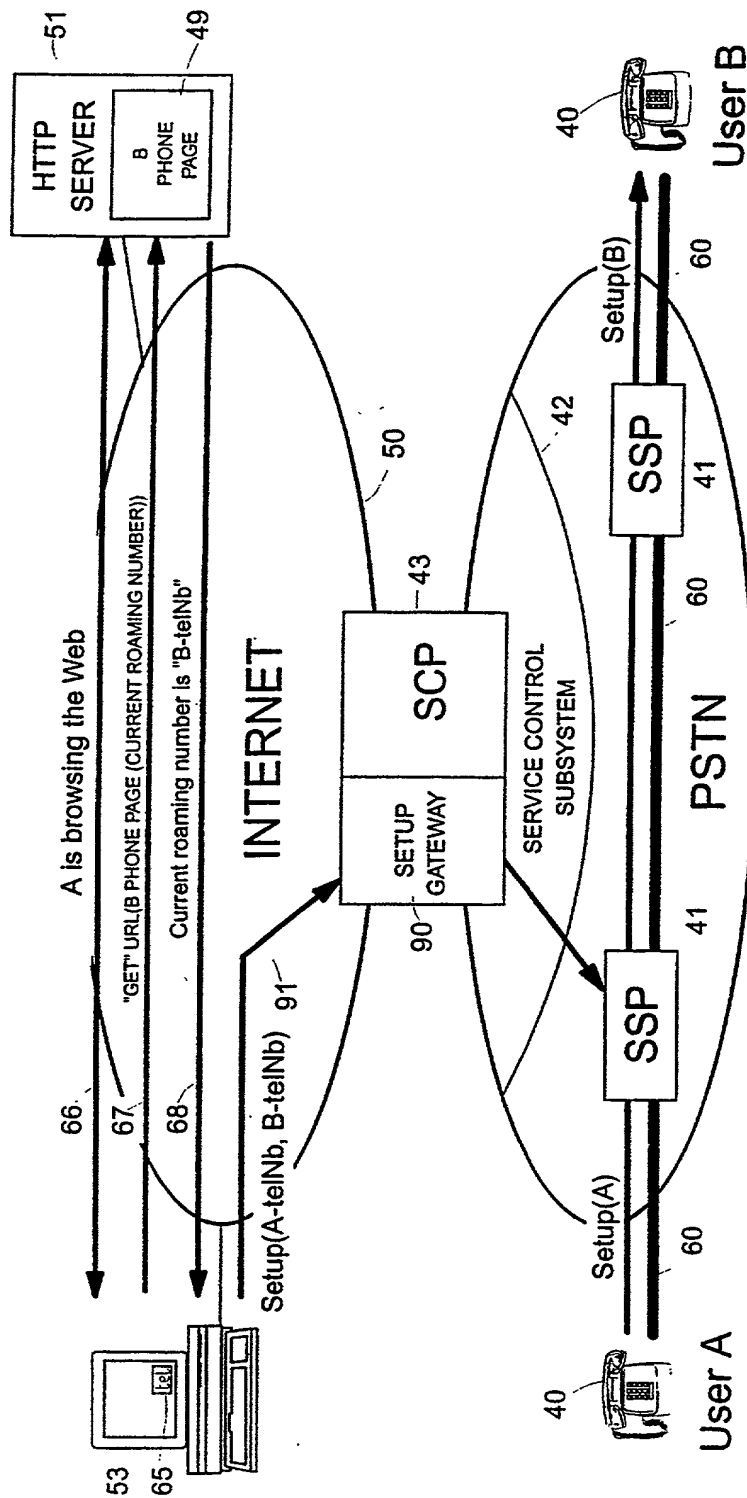


FIG. 16

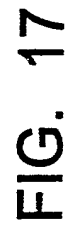


FIG. 17

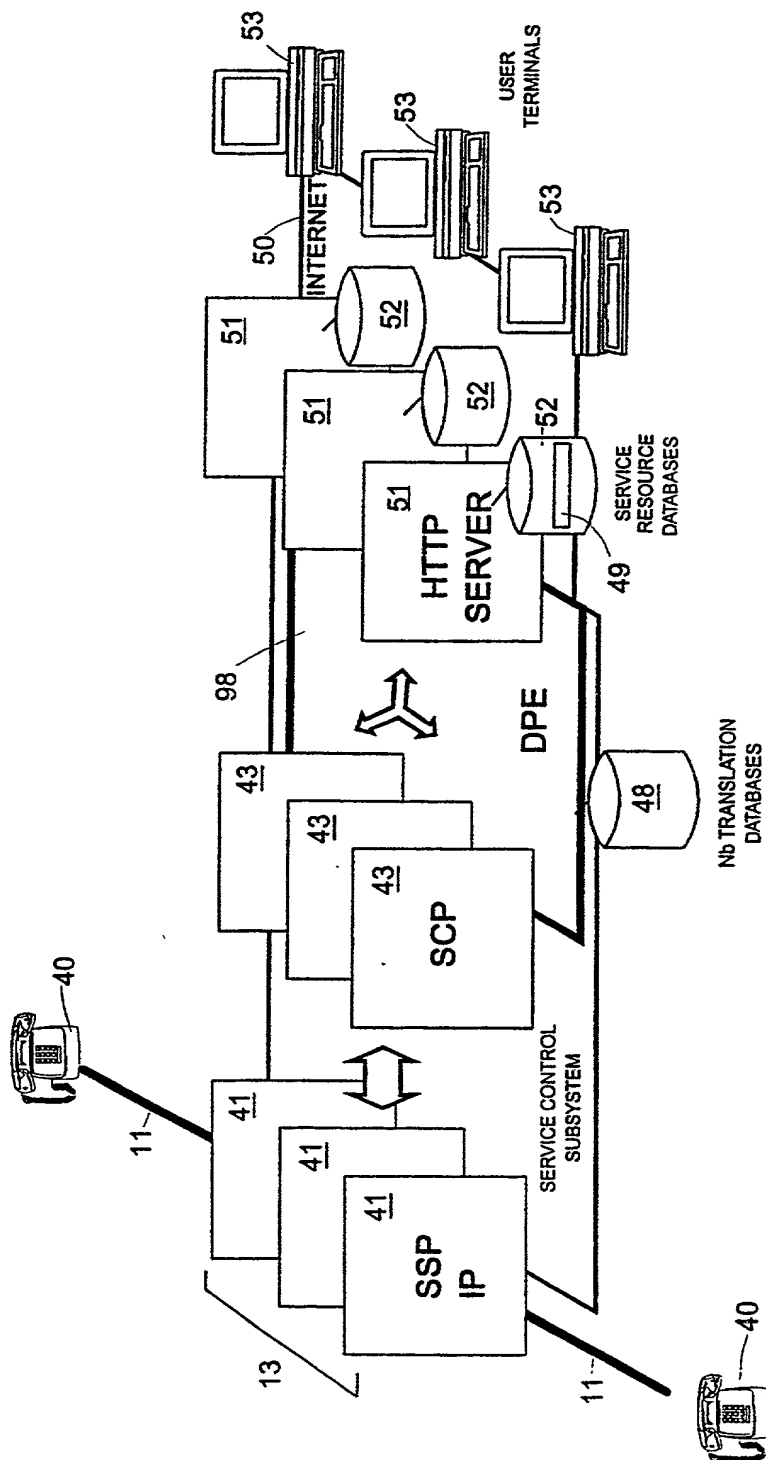


FIG. 18